

| | Type | Hits | Search Text |
|----|------|-------|--------------------------|
| 1 | IS&R | 160 | (327/313).CCLS. |
| 2 | IS&R | 167 | (327/312).CCLS. |
| 3 | BRS | 153 | S6 not S1 |
| 4 | IS&R | 1209 | (327/309-310).CCLS. |
| 5 | BRS | 609 | S11 and transistor |
| 6 | BRS | 524 | S11 not S12 |
| 7 | BRS | 1133 | S10 not (S1 S6) |
| 8 | IS&R | 1 | ("6531924").PN. |
| 9 | IS&R | 167 | (327/318).CCLS. |
| 10 | BRS | 107 | S17 not (S1 S6 S10) |
| 11 | IS&R | 288 | (327/327-328).CCLS. |
| 12 | BRS | 180 | S19 not (S1 S6 S10 S17) |
| 13 | IS&R | 380 | (361/90,90.1).CCLS. |
| 14 | BRS | 4847 | limiting adj circuit |
| 15 | BRS | 1181 | S22 and transistor |
| 16 | BRS | 1548 | S22.ti. |
| 17 | IS&R | 1 | ("5373202").PN. |
| 18 | BRS | 21175 | clamp\$3.ti. |
| 19 | BRS | 105 | voltage adj clamp\$3.ti. |
| 20 | BRS | 443 | voltage adj clamp\$3.ti. |

| | Type | L # | Hits | Search Text | DBs |
|----|------|-----|--------|--|---|
| 1 | IS&R | L1 | 1 | ("6552694").PN. | US-PGPUB; USPAT |
| 2 | IS&R | L2 | 1 | ("6552594").PN. | US-PGPUB; USPAT |
| 3 | IS&R | L4 | 554 | (361/92).CCLS. | US-PGPUB; USPAT |
| 4 | IS&R | L5 | 1980 | (361/56).CCLS. | US-PGPUB; USPAT |
| 5 | IS&R | L13 | 227 | (361/57).CCLS. | US-PGPUB; USPAT |
| 6 | BRS | L14 | 207 | 13 not 5 | US-PGPUB; USPAT |
| 7 | BRS | L15 | 303550 | protect\$3.ti. | USOCR; EPO; JPO; DERWENT; IBM_TDB |
| 8 | BRS | L16 | 12007 | 15 and transistor | USOCR; EPO; JPO; DERWENT; IBM_TDB |
| 9 | BRS | L17 | 7395 | 16 and voltage | USOCR; EPO; JPO; DERWENT; IBM_TDB |
| 10 | BRS | L18 | 315 | 16 and current adj source | USOCR; EPO; JPO; DERWENT; IBM_TDB |
| 11 | BRS | L19 | 4029 | 16 and (input I/O) | USOCR; EPO; JPO; DERWENT; IBM_TDB |
| 12 | BRS | L20 | 2999 | 19 and voltage | USOCR; EPO; JPO; DERWENT; IBM_TDB |
| 13 | BRS | L21 | 449 | DC adj DC adj converter.ti. | US-PGPUB; USPAT |
| 14 | IS&R | L23 | 1 | ("5856760").PN. | US-PGPUB; USPAT |
| 15 | BRS | L22 | 324 | 21 and switch | US-PGPUB; USPAT |
| 16 | BRS | L26 | 140 | (comparator with (clamp\$3 limit\$3) with decod\$3) | US-PGPUB; USPAT |
| 17 | BRS | L27 | 765 | 16 and esd | USOCR; EPO; JPO; DERWENT; IBM_TDB |
| 18 | BRS | L28 | 596 | 27 not 20 | USOCR; EPO; JPO; DERWENT; IBM_TDB |

| | Type | L # | Hits | Search Text | DBs |
|----|------|-----|------|--|--------------------|
| 19 | BRS | L29 | 803 | current adj limit\$3 same transistor same current adj source | US-PGPUB; USPAT |
| 20 | BRS | L30 | 1482 | (current near2 limit\$3) same transistor same current adj source | US-PGPUB; USPAT |
| 21 | BRS | L31 | 155 | 30.clm. | US-PGPUB; USPAT |
| 22 | BRS | L32 | 1 | 363/15-33 | US-PGPUB; USPAT |
| 23 | IS&R | L33 | 4407 | (363/15-33).CCLS. | US-PGPUB; USPAT |
| 24 | BRS | L34 | 3513 | 33 and (decoder limit\$3 clamp\$3 protect\$3) | US-PGPUB; USPAT |
| 25 | BRS | L35 | 2760 | 34 and transistor | US-PGPUB; USPAT |
| 26 | BRS | L36 | 1074 | 35 and comparator | US-PGPUB; USPAT |
| 27 | BRS | L37 | 182 | voltage adj limit\$3 same transistor same current adj source | US-PGPUB; USPAT |